What is claimed is:

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1. A method comprising:

streaming at least two independent video sources for display on a video display screen; and

causing said sources to be displayed at separate regions of said display screen.

2. The method of claim 1 including forming said sources into packets in a first device and transporting said packets to a second device.

3. The method of claim 2 including depacketizing said packets in said second device.

4. The method of claim 1 including transmitting said video sources from a processor-based system to a display device including said display screen.

5. The method of claim 4 including transmitting said video sources over a wireless connection between said processor-based system and said display device.

6. The method of claim 1, wherein said display screen includes a pixel array and a memory array, refreshing said memory array and said pixel array in the same refresh cycle.

7. The method of claim 6 including displaying said sources on a display that uses liquid crystal over semiconductor technology.

8. The method of claim 1 including streaming video sources for display on said display screen at different frame rates.

9. The method of claim 1 wherein one of said video sources includes television programming and the other of said video sources includes graphical information.

10. The method of claim 1 including streaming a first video source that includes television programming information and a second video source that includes an electronic programming guide information.

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11. A system comprising:

a processor;

storage coupled to said processor;

a video controller coupled to said processor;

a packetization device coupled to said video controller which independently packetizes at least two ~~video streams~~.

12. The system of claim 11 including a modulation device to modulate and transport said independently packetized streams.

13. The system of claim 11 wherein each of said video streams has a different frame rate and is packetized to be de-packetized at the original frame rate in a display device.

14. An article comprising a medium storing instructions that cause a processor-based system to:

receive two independent video sources; and

packetize each of said video sources so that they may be displayed in separate regions of a display screen.

15. The article of claim 14 further storing instructions that cause a processor-based system to transmit said video sources from said processor-based system to a display device including a display screen.

16. The article of claim 15 further storing instructions that cause the processor-based system to transmit said video sources over a wireless connection between said processor-based system and said display device.

17. The article of claim 16 further storing instructions that cause the processor-based system to transmit said video sources for display on said display screen at different frame rates.

18. A system comprising:

a semiconductor substrate;

a liquid crystal over semiconductor pixel array formed in said substrate;

a memory coupled to said array, said memory also formed in said substrate; and

a device for receiving a signal made up of a plurality of independent video sources and driving each of said video sources for display on a different portion of said pixel array.

19. The system of claim 18 wherein said system includes a device that de-packetizes said signal to form independent video sources for display on said pixel array.

20. The system of claim 19 wherein said pixel array includes a plurality of pixels including a memory cell.

21. The system of claim 20 wherein said memory cells are static random access memory cells.

22. The system of claim 19 wherein said pixel array is coupled to said memory by a digital to analog converter.

23. The system of claim 19 wherein said memory includes a cell associated with each of a plurality of pixels of said pixel array.

24. A system comprising:

an imaging device having a plurality of imaging elements;

a memory that receives and stores at least two independent video sources; and

a controller that drives said video sources onto separate portions of said imaging device.

25. The system of claim 24 wherein said imaging device is a thin film transistor imaging device.

26. The system of claim 24 wherein said imaging device is a cathode ray tube.

27. The system of claim 24 wherein said imaging device uses liquid crystal over semiconductor technology.

28. The system of claim 24 including a device to receive packetized video information, de-packetize said

information and provide said de-packetized information to said memory as independent video sources.